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PATENT  
068167.0107

Pursuant to 37 C.F.R. § 1.8, I hereby certify that I have information and a reasonable basis for belief that this correspondence will be deposited as first-class mail with the United States Postal Service in an envelope addressed to the Commissioner for Patents, Washington, D.C. 20231 on April 3, 2002.

*K. Z. Z.*  
Signature

4-3-02  
Date

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Giles, et al.

Serial No.: 09/809,731

Filed: March 15, 2001

Invention: Method for Hybrid Processing of  
Software Instructions of an  
Emulated Computer System

Commissioner for Patents  
Washington, D.C. 20231

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Group No.: 2132

Examiner: Not Yet Assigned

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## SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Applicant respectfully requests, pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, that the art listed on the attached PTO-1449 form be considered and cited in the examination of the above-identified application. A copy of the cited art is enclosed for the convenience of the Examiner. Pursuant to 37 C.F.R. §§1.97(g) and (h), no representation is made that these references are material to the patentability of the present application.

The information disclosure statement submitted herewith is being submitted before the mailing of the first office action on the merits. Applicants believe that no fee is required. If a fee is required, please charge any fee to Deposit Account No. 02-0383 of Baker Botts L.L.P.

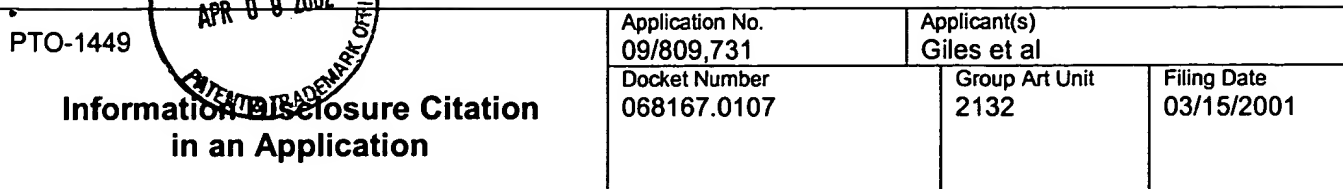
Respectfully submitted,



Roger Fulghum  
Registration No. 39,678

Baker Botts L.L.P.  
One Shell Plaza  
910 Louisiana  
Houston, TX 77002-4995  
(713) 229-1707  
Attorney Docket No.: 068167.0107

Date: April 3, 2002

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		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	
A		U.S. Patent Application Serial No. 09/617,709, "System and Method for Emulating the Operation of a Translation Look-Aside Buffer", Traut, filed July 17, 2000	
B		U.S. Patent Application Serial No. 09/617,669, "System and Method for Displaying Current Images of Virtual Machine Environments", Traut, et al., filed July 17, 2000	
C		U.S. Patent Application Serial No. 09/617,624, "System and Method for Emulating the Operation of a Video Graphics Adapter", Carroll, et al., filed July 17, 2000	
D		U.S. Patent Application Serial No. 09/747.492, "System and Method for the Logical Substitution of Processor Control in an Emulated Computing Environment", Traut, filed December 21, 2000	

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.